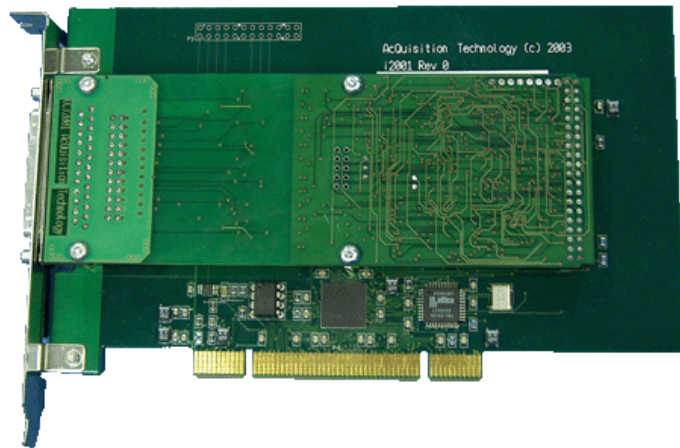


PCI302

ARINC-429 Controller for PCI



The PCI302 provides four ARINC-429 compatible channels (2x receive and 2x transmit) at any rate between 12.5 and 100kbps.

Because the PCI302 is built upon a flexible platform, the user functions can be implemented almost without restriction, making this product flexible and user-friendly.

Features:

- > Flexible platform allows easy addition of user-specific requirements
- > 2 transmit channels, 2 receive channels
- > Data rate software adjustable between 12.5 and 100kbps per channel
- > 16 User-selectable labels for interrupt generation upon reception
- > Status and statistics monitoring for all channels
- > ARINC-429 rise/fall capacitors selectable per channel
- > Provides excellent platform for ARINC-429 protocol analyses

PCI Interface

* The PCI302 has a PCI Specification 2.2 compliant target interface and uses a PCI9030 PCI Bus Target Interface Chip by PLX Technology Inc.

Controller

* The onboard MC68332 32-bit controller executes firmware from local Flash memory, providing a versatile and user-friendly ARINC-429 interface.

Receiving / Transmitting Data

Receiving:

* For each receive channel, a local buffer is available for each label. After its parity is checked, the received message is stored in the corresponding label buffer, overwriting the previous content. The last received message for a specific label can be retrieved by issuing a command from the host to the firmware interface.

Transmitting:

* A message is transmitted by passing it as a parameter to the command interface, and initiate the transmission by issuing the appropriate command. Parity generation and checking is done on the PCI302 transparent for the user.

Interrupt Facilities

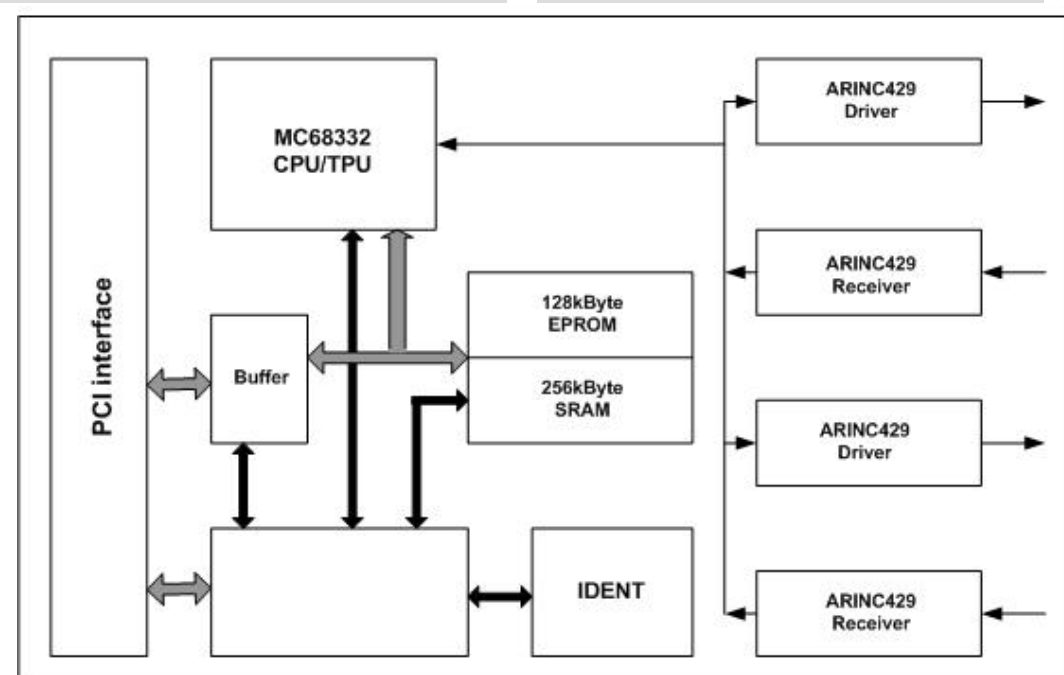
* The user can select up to 16 labels for interrupt generation upon reception of data.
* This feature provides a mechanism that allows a user to monitor a label without data-loss.

Statistics

* For each channel and for each label, the number of transmitted and received messages are logged into the statistics part of the local firmware.
* Parity errors are logged global to a channel.

Ordering Information

* **PCI302/T02** PCI card, 0x Xmit, 2x Recv
* **PCI302/T03** PCI card, 1x Xmit, 0x Recv
* **PCI302/T04** PCI card, 1x Xmit, 2x Recv
* **PCI302/T05** PCI card, 2x Xmit, 0x Recv
* **PCI302/T01** PCI card, 2x Xmit, 2x Recv
* **PCI302/SW** APIS based software
* **PCI302/MAN** manual on paper



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